

S. B. Roll. No.....

MICROPROCESSORS
4th Exam/ECE/2595/Nov'24
(For 2018 Batch Onwards)

Duration: 3Hrs.

M.Marks:75

SECTION-A

Q1. Do as directed.

15x1=15

- a. TRAP has the highest priority. (T/F)
- b. Stack Pointer (SP) points to the next instruction to be executed. (T/F)
- c. DMA stands for_____
- d. PSW stands for_____.
- e. 8085 microprocessor contains _____ flags
- f. 8251 is a programmable peripheral interface (PPI). (T/F)
- g. _____ is lowest priority interrupt
- h. ISR stands for _____
- i. Program Counter is _____ bit register.
- j. 8085 has _____ address lines.
- k. 8085 microprocessor has _____ pin IC.
- l. RAM is a _____ memory
- m. 8085 microprocessor has _____ addressing mode
- n. A nibble is _____ bits.
- o. The no. of general purpose register is _____

SECTION-B

Q2. Attempt any six questions.

6x5=30

- i. Explain the bus organization of 8085.
- ii. Explain the software interrupts
- iii. Explain the addressing modes of 8085.
- iv. Explain six data transfer instructions of 8085.
- v. Draw the pin diagram of 8085.
- vi. What is interrupt service subroutine?
- vii. What are the applications of microprocessors?
- viii. Explain the instruction format of 8085.

SECTION-C

Q3. Attempt any three questions.

3x10=30

- a. Discuss instruction timing and cycles.
- b. What is an Interrupt? Explain various interrupts of 8085
- c. Draw and explain block diagram of 8085
- d. Explain various instructions of 8085.
- e. Write a short note on i) 8255 PPI ii) 8257 DMA controller